

# Pipelining

Multiple CPUs carrying out a different part of the fetch-decode-execute cycle.

As long as it is kept full, makes maximum use of CPU

Single Instruction  
Single Data  
Computer (SISD)

# Array Processing

Where data is arranged into arrays and all acted upon by the same instruction

Relies on the fact that all the sets of data are being acted on by the same instruction

Single Instruction  
Multiple Data  
Computer (SIMD)

# Multi-core processing

Where several serial processors operate simultaneously on different parts of a job

Dependant on being able to cut problems down into chunks

Multiple  
Instruction  
Multiple Data  
Computer (MIMD)